

REPLACEMENT SHEET

2/11

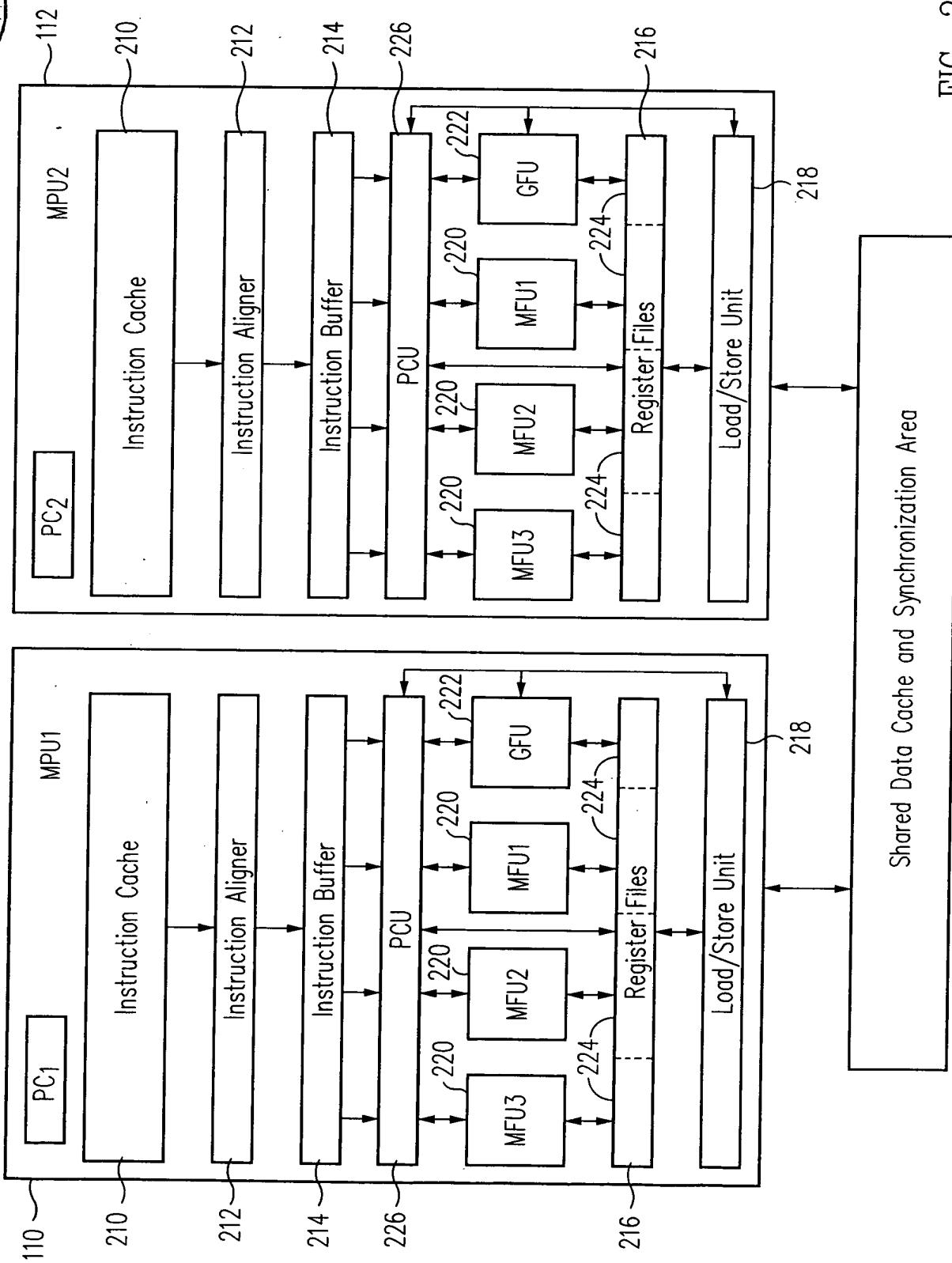


FIG. 2

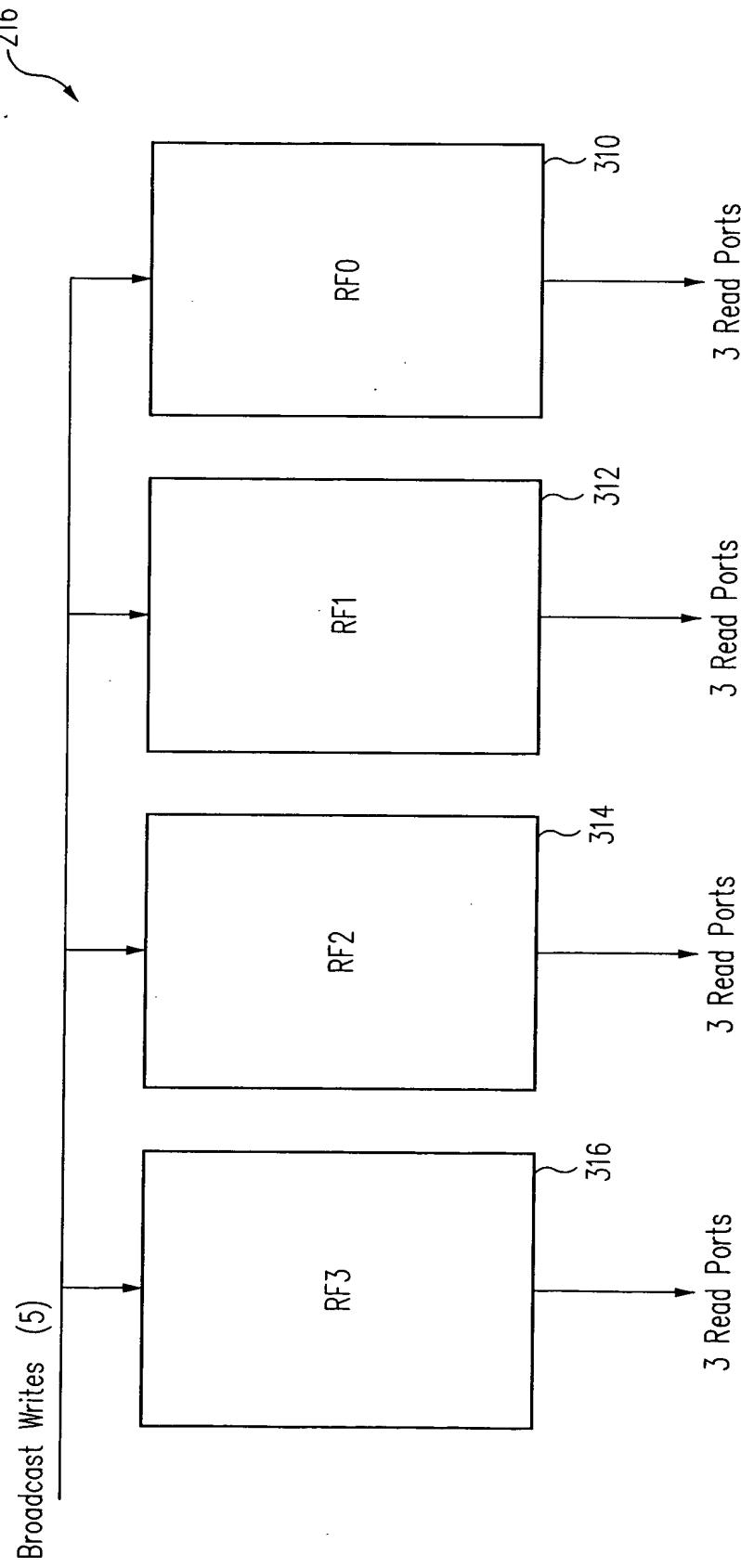


FIG. 3

09/204,585

4/11

REPLACEMENT SHEET

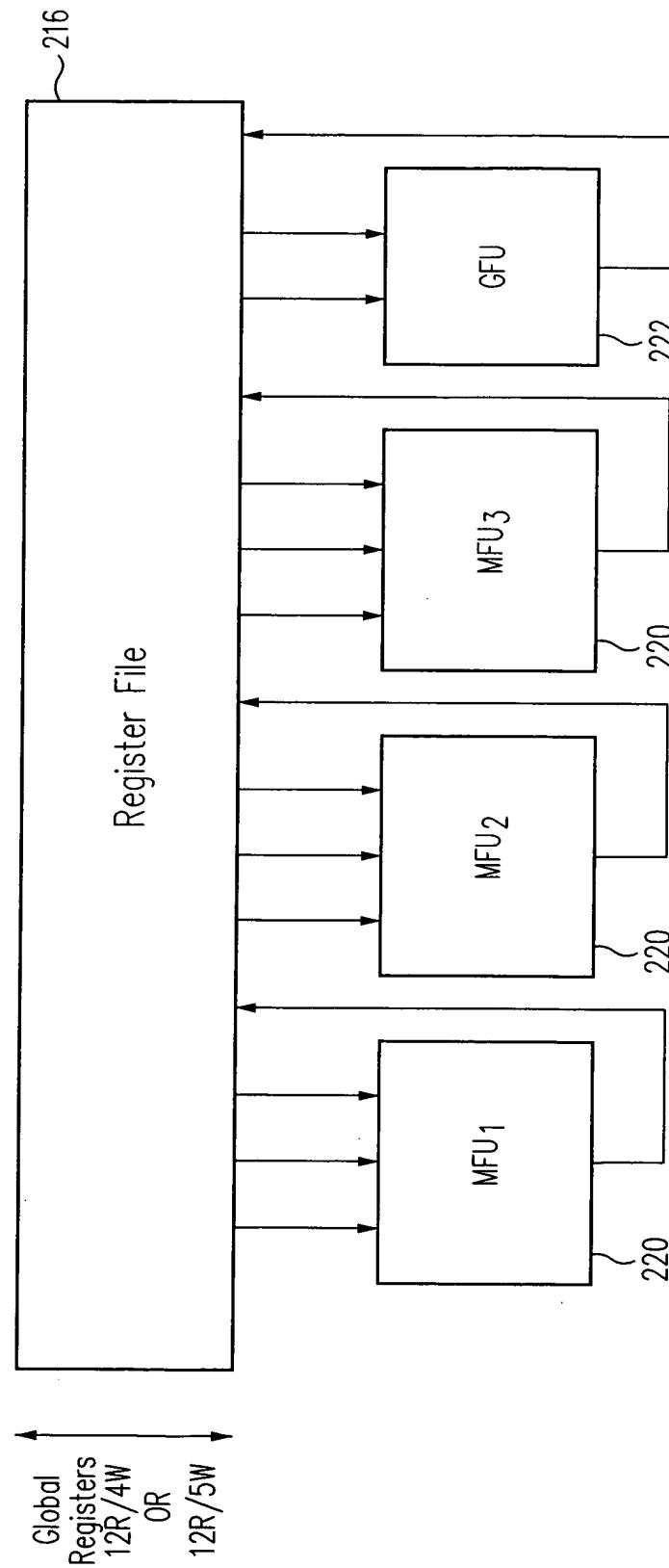


FIG. 4

09/204,585

REPLACEMENT SHEET

7/11

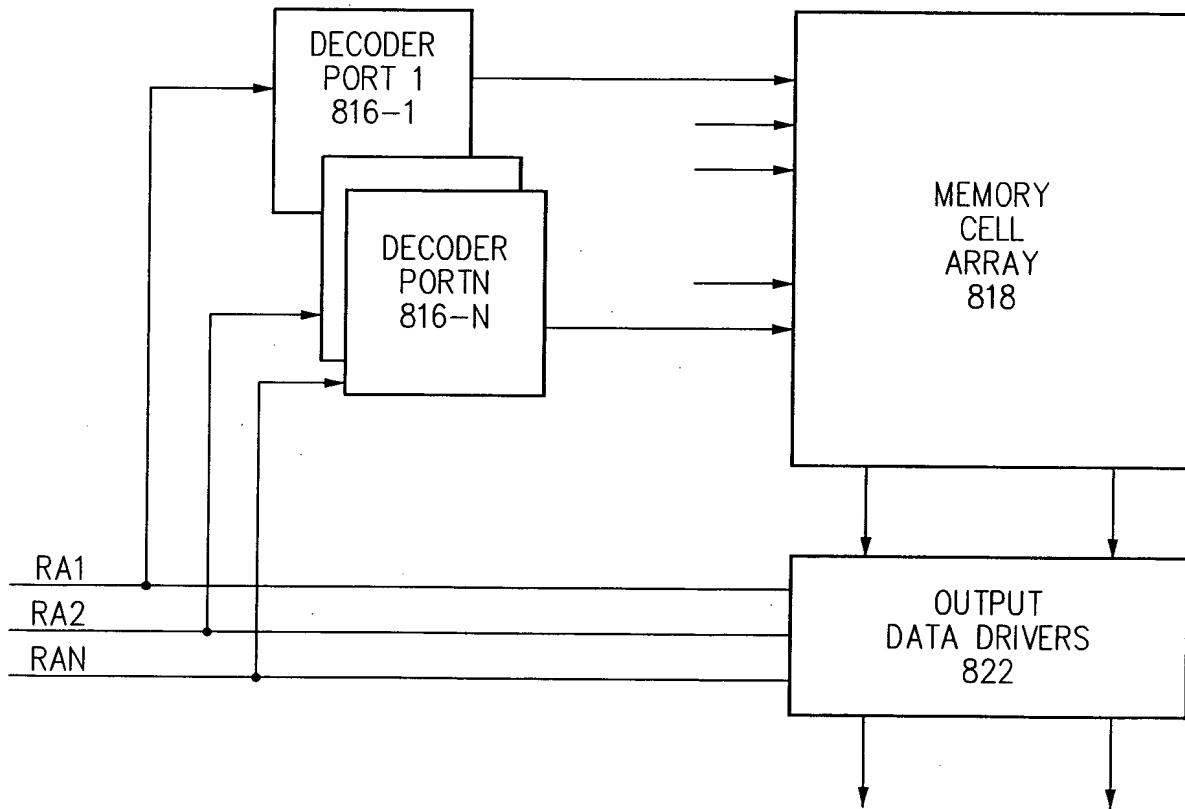


FIG. 7

09/204,585

8/11

REPLACEMENT SHEET

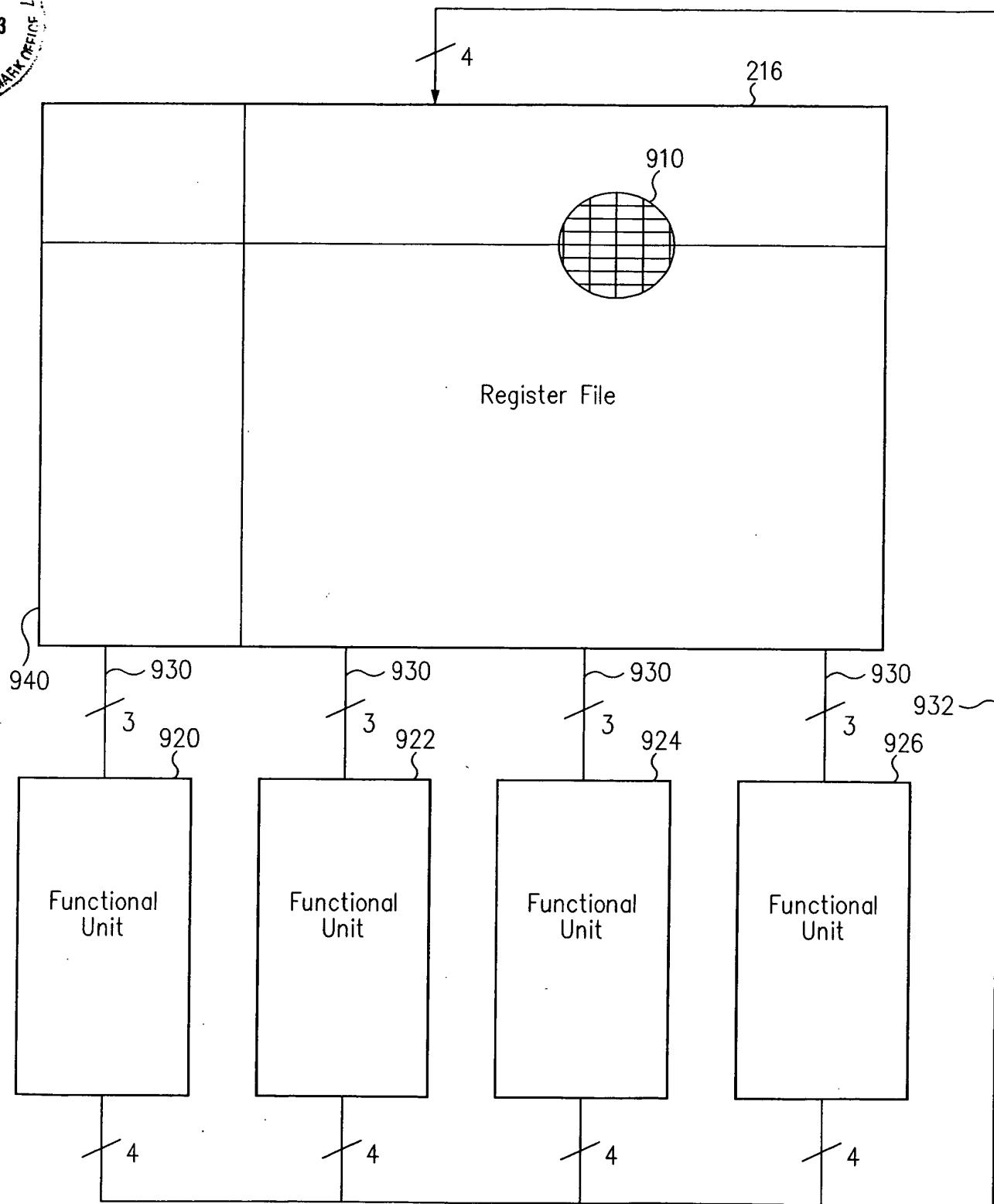


FIG. 8A

Local and Global Register Partitioning in VLIW Processor
Marc Tremblay and William Joy

09/204,585

REPLACEMENT SHEET

9/11

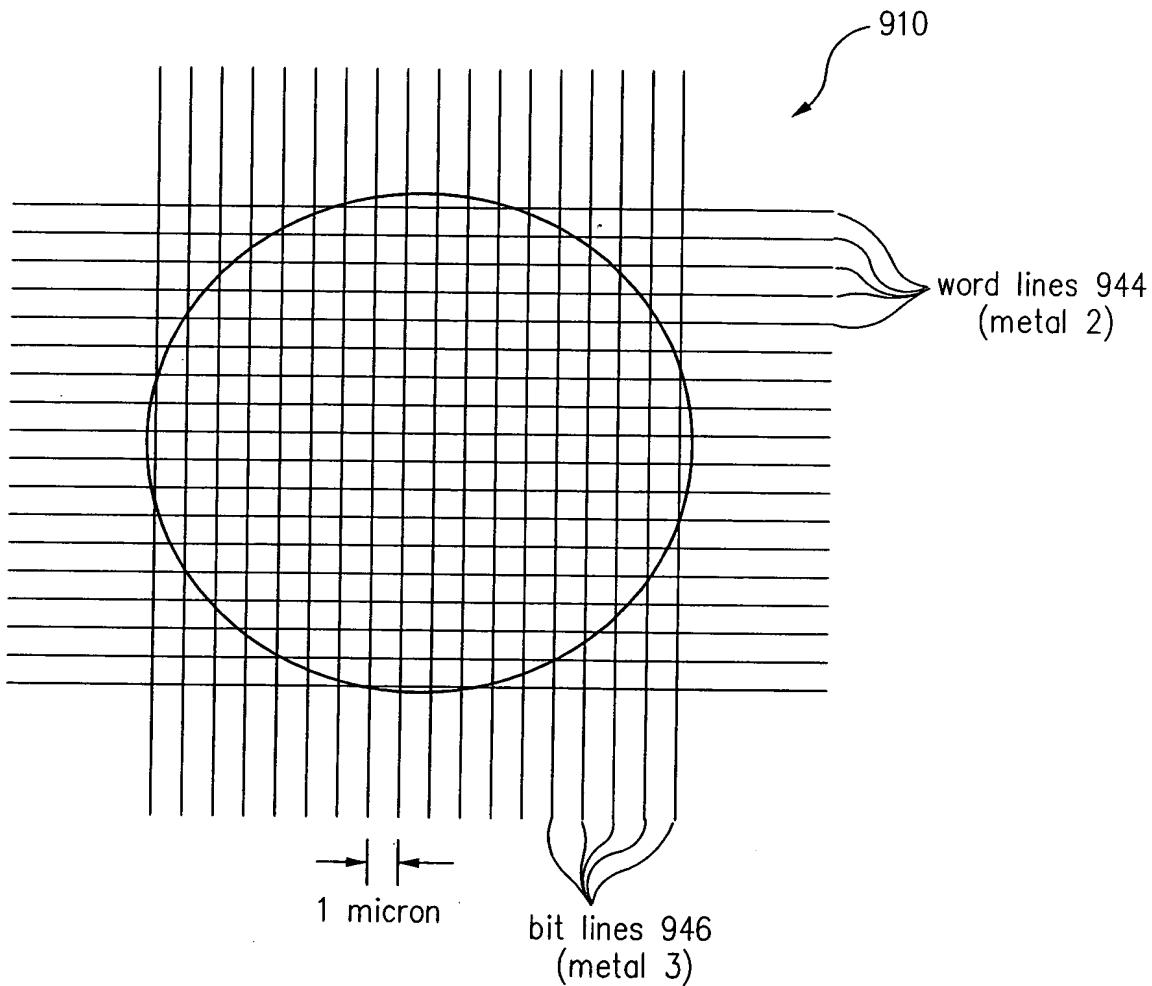


FIG. 8B

REPLACEMENT SHEET

10/11

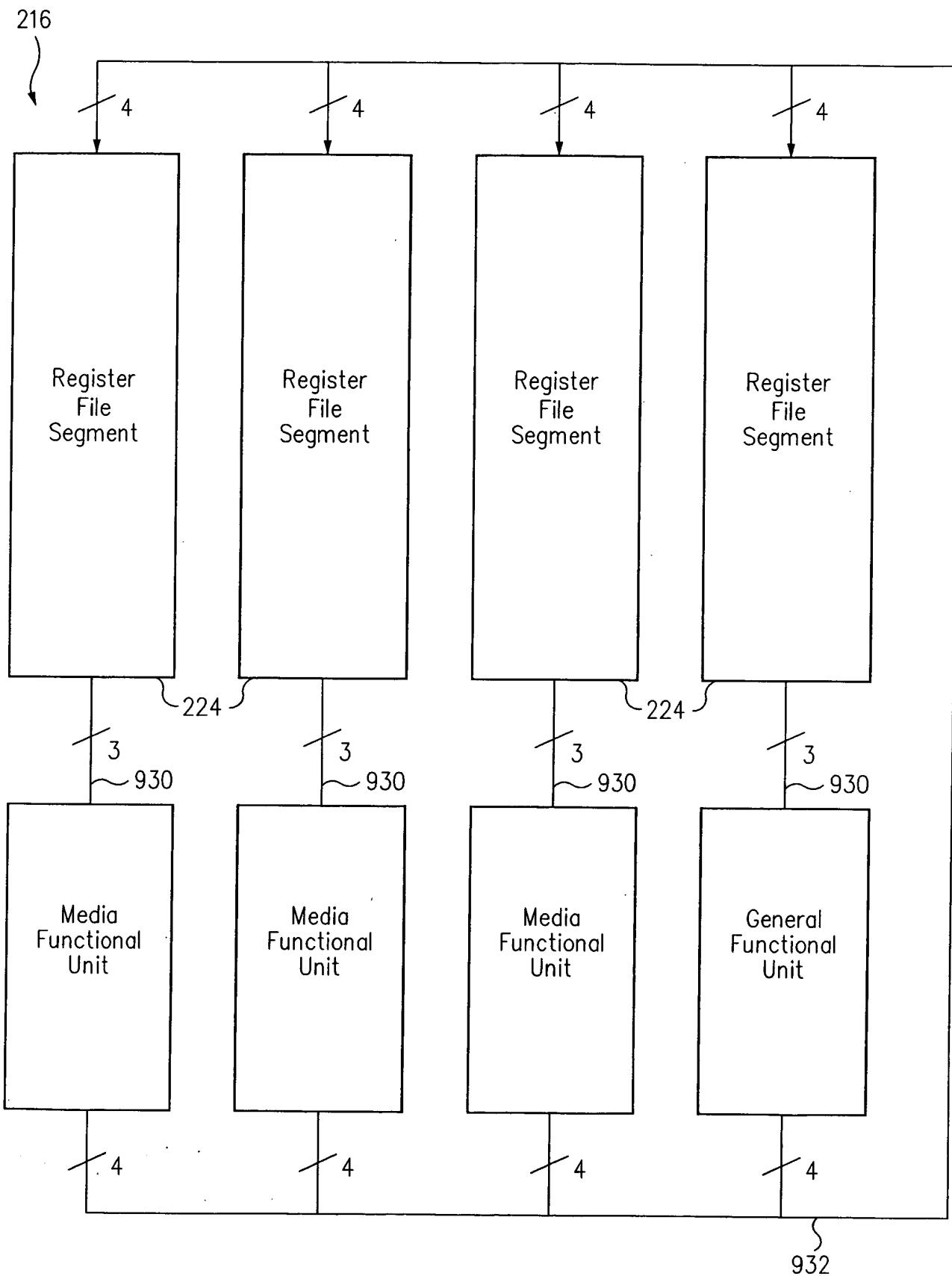


FIG. 9

09/204,585

REPLACEMENT SHEET

11/11

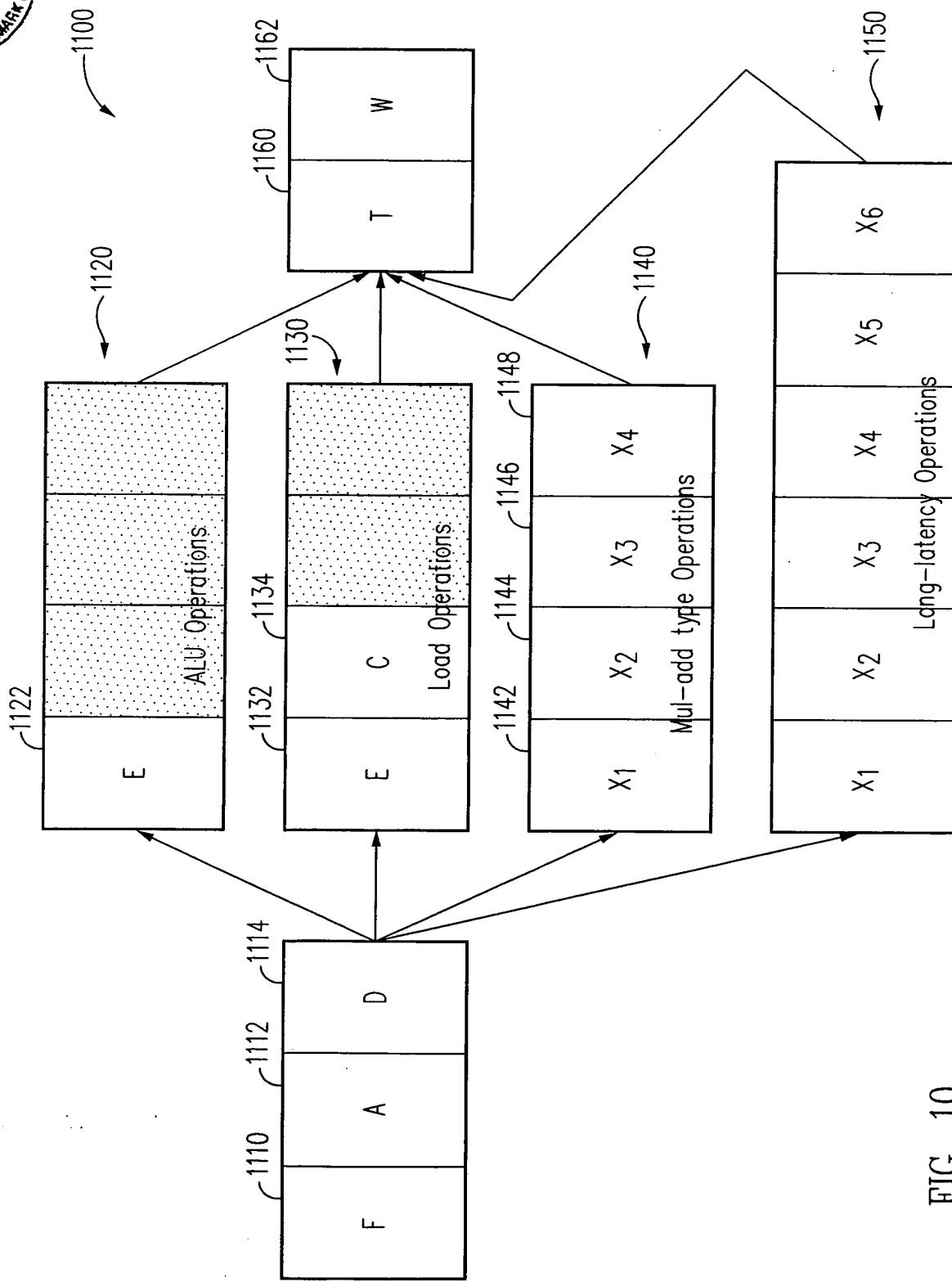


FIG. 10